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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10016540	FILING DATE 10/26/2001	CLASS 370-710	SUBCLASS 11	GAU 2564	EXAMINER <i>Gulkin</i>
**APPLICANTS: Boxer Lawrence; Castagnozzi Dan; 216 2					
**CONTINUING DATA VERIFIED: THIS APPLN CLAIMS BENEFIT OF 60/245,895 11/03/2000					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input checked="" type="checkbox"/> DO NOT PUBLISH		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		AMCC-001XX			
Verified and Acknowledged Examiners's initials					
TITLE : Parallel data bus with bit position encoded on the clock wire					

U.S. DEPT. OF COMMERCE / PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner			
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
			Print Fig.
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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